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In contrast, JP-11228508 A2 was published on August 24, 1999 and deals with the purification of a compound for use as an organic electroluminescent material. An English abstract of JP-11228508 A2 is also attached.

The above remarks have been submitted to clarify the record.

If desired by the Examiner, Applicants will submit a corrected Declaration. However, in view of the above, Applicants submit that it is necessary to submit such a corrected Declaration.

If the Examiner has any questions or comments, please contact Craig A. McRobbie, Reg. No. 42,874 at the telephone number listed below.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

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GMM/CAM:bmp

Attachments: English abstract of JP-11288508 A2 English abstract of JP-11228508 A2

(Rev. 02/13/2004)





Patent number:

JP11288508

Publication date:

1999-10-19

Inventor:

KUSAKABE MASAYOSHI; KOBAYASHI MASAHIRO

Applicant:

NIPPON ELECTRIC GLASS CO LTD

Classification:

- international:

G11B5/82; G11B5/84

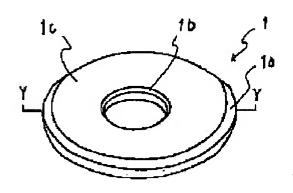
- european:

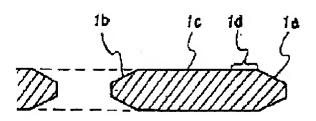
Application number: JP19980106973 19980401

Priority number(s):

Abstract of JP11288508

PROBLEM TO BE SOLVED: To evade that a chamfered part is chipped, and a magnetic layer is damaged with a fragment by eliminating a protrusion part from a connection part from the chamfered part to a substrate surface and forming the whole area of the substrate surface having a specified value or below in a surface roughness value. SOLUTION: The whole area of the glass substrate 1 surface for a magnetic disk is formed having the surface roughness Ra value of <=1.5 nm. The glass substrate 1 is constituted so that the protrusion part doesn't exist on the circular connection part 1d from the chambered parts 1a, 1b chamfered by etching liquid on respective outer peripheral and inner peripheral end surfaces to the substrate surface 1c. When the glass substrate 1 is manufactured, first of all, the surface of a glass board consisting of boron silicic acid glass and ground to have the surface roughness Ra value of <=1.5 nm is coated/protected by hotmelt type adhesive containing poly acetic acid vinyl, and a doughnut-shaped glass board is formed. It is immersed in the etching liquid containing fluorosulfuric acid for a prescribed time to be cleaned/dried, and the glass substrate 1 having the unbreakable chamfered parts 1a, 1b is obtained.





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PATENT ABSTRACTS OF JAPAN

(11)Publication number:

11-228508

(43) Date of publication of application: 24.08.1999

(51)Int.Cl.

C07C211/54 C07C209/84

(21)Application number: 10-049975

(71)Applicant: CASIO COMPUT CO LTD

(22)Date of filing:

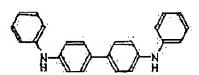
(72)Inventor: YAZAWA TOMOYA

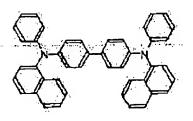
(54) PURIFICATION OF COMPOUND

(57)Abstract:

PROBLEM TO BE SOLVED: To provide a method for purifying compounds in good yield by subjecting the mixed compounds of the first and second compounds to a multistep chromatography using a solvent capable of dissolving the only second compound as the first mobile phase, and a solvent capable of dissolving the first compound as the second mobile phase.

SOLUTION: A mixture of the first compound such as N,N'diphenylbenzidine of formula I and N,N'-diphenyl-N,N'-bis(1naphthyl)-1,1'-biphenyl-4,4'-diamine of formula II, and the second compound such as 3-iodotoluene and 1iodonaphthalene capable of dissolving the first compound is subjected to the first chromatography using a solvent hexane capable of not or hardly dissolving the first compound and capable of dissolving the second compound as a mobile phase to isolate the second compound, and the residue adsorbed on the fixed layer is purified by subjecting the residue to the second chromatography by using toluene/hexane mixed solvent capable of dissolving the first compound as the mobile phase in the method for purifying the compound.





LEGAL STATUS

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[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

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